

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: S. NARUI et al.
Serial No.: (new Continuation Application under 37 CFR §1.53[b] of
U.S. Application Serial No. 09/714,127
Filed: August 19, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND METHOD FOR MANUFACTURING THE SAME
Group: 2811
Examiner: S. HU

CLAIM FOR FOREIGN PRIORITY

MS: PATENT APPLICATIONS

Commissioner for Patents
POB 1450
Alexandria, VA 22313-1450

Aug. 19, 2003

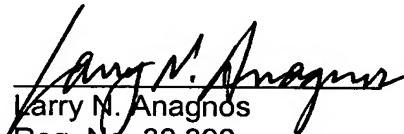
Sir:

Pursuant to 35 U.S.C. §119 and 37 CFR §1.55, applicants hereby claim
the right of priority based on the following foreign application filed in Japan:

Application No. 8-3648, filed January 12, 1996.

A certified copy of this foreign application was filed in U. S. application
Serial No. 08/782,351, filed on January 13, 1997. .

Respectfully submitted,
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